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(71) Applicant: Hewlett-Packard Company
3000 Hanover Street
Palo Alto California 94304(US)

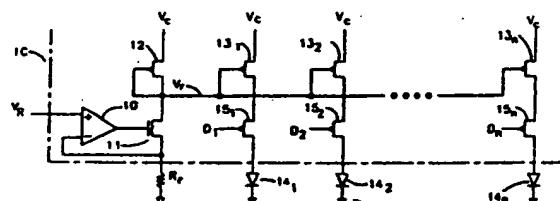
(72) Inventor: Kalata, Steven
1235 Wildwood Avenue No. 383
Sunnyvale, California 94089(US)
Inventor: Reid, Donald M.
3325 N.W. McKinley Drive
Corvallis, Oregon 97330(US)
Inventor: Brown, Charles A.
1505 N.W. Woodland Drive
Corvallis, Oregon 97330(US)
Inventor: Thayer, Billy E.
1616 N.W. 29th Street
Corvallis, Oregon 97330(US)

(74) Representative: Colgan, Stephen James et al
CARPMAELS & RANSFORD 43 Bloomsbury
Square
London WC1A 2RA(GB)

(54) Light emitting diode array current power supply.

(57) A power supply circuit for an LED print head has a reference current source (10,11,12,R_r) connected to a printer system reference voltage (V_r) for providing a second reference voltage (V_c) for an individual, integrated circuit chip (IC). A plurality of substantially similar field effect transistors (13) have their gates connected to the chip reference voltage (V_c) for providing substantially similar current to each of a plurality of light emitting diodes (14). The current is enabled to each such LED (14) by the presence or absence of a data signal (D). A reference resistor (R_r) in the reference current source permits adjustment so that each of a plurality of integrated circuit chips (IC) can provide substantially similar current to each of a plurality of LED dice, or adjust the current level, if required, to achieve similar light output from each LED (14).

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LIGHT EMITTING DIODE ARRAY CURRENT POWER SUPPLY

Background of the Invention

It has become desirable to employ non-impact xerographic-type printers for text and graphics. In such a printer, an electrostatic charge is formed on a photoreceptive surface of a moving drum or belt, and selected areas of the surface are discharged by exposure to light. A printing toner is applied to the drum and adheres to the areas having an electrostatic charge and does not adhere to the discharged areas. The toner is then transferred to a sheet of plain paper and is heat-fused to the paper. By controlling the areas illuminated and the areas not illuminated, characters, lines and other images may be produced on the paper.

One type of non-impact printer employs an array of light emitting diodes (commonly referred to herein as LEDs) for exposing the photoreceptor surface. A row, or two closely spaced rows, of minute LEDs are positioned near an elongated lens so that their images are arrayed across the surface to be illuminated. As the surface moves past the line of LEDs, they are selectively activated to either emit light or not, thereby exposing or not exposing, the photoreceptive surface in a pattern corresponding to the LEDs activated.

To form good images in an LED printer, it is desirable that all of the light emitting diodes produce the same light output when activated. This assures a uniform quality image all the way across a paper. The light output from an LED depends on a number of factors including current, temperature, and processing parameters for forming the LED which may affect its light output as a function of current.

Light emitting diodes for print heads are formed on wafers of gallium arsenide or the like, suitably doped to conduct current and emit light. Long arrays of LEDs are formed on a wafer which is cut into separated dice, each having an array of LEDs. A row of such dice are assembled end-to-end to form a print head array. The light output of the LEDs on a given die are usually reasonably uniform, however, there may be variations from die to die as processing parameters differ between dice. There is some variation within dice from an individual wafer and greater variation from wafer to wafer.

The LEDs are driven by power supplies on integrated circuit chips. The current output of these chips may also vary depending on processing parameters in making these chips. Such variations may compound the variations in the LED dice.

A parameter that is partly LED power supply dependent is the rise time for current flow. This is

significant, since the exposure of the photoreceptive surface is a function of both intensity and illumination time. In an LED print head, there may be a few thousand LEDs across the width of the photoreceptive surface. The current in each LED may also be affected by the number of LEDs enabled at any time. Thus, there may be a relatively high current and concomitant higher light intensity or total exposure when a few LEDs are enabled, as compared with the current and light output when a very large number of LEDs are enabled.

It is desirable, therefore, to provide a power supply for an array of LEDs which assures uniform light output across the array and a light output substantially independent of differences in the number of LEDs enabled.

Brief Summary of the Invention

There is, therefore, provided in practice of this invention according to a presently preferred embodiment, a power supply for a light emitting diode print head. The power supply is formed on a plurality of integrated circuit chips where each chip has a plurality of outputs for supplying current to each of a plurality of LEDs. Each such integrated circuit chip has a constant current source connected to a reference voltage for the entire print head. Preferably, a resistor is included in the constant current source for each chip to permit setting the current, and hence, a chip reference voltage for all of the LED current sources on the chip. A current is applied to each LED which is a function of the chip reference voltage on the respective chip and the presence or absence of a data signal for the respective LED.

In a preferred embodiment, such a power supply has a reference field effect transistor (FET) in the constant current source and a plurality of current source FETs substantially similar to the reference FET, with each current source FET having an output connected to a respective LED. The gates of all of the FETs are interconnected with the output of the reference FET so that each of the FETs passes the same current when enabled by a data signal for the respective LED.

Brief Description of the Drawings

These and other features and advantages of the present invention will be appreciated as the same becomes better understood by reference to

the following detailed description when considered in connection with the accompanying drawings wherein:

FIG. 1 illustrates in block form a plurality of integrated circuit chips for an exemplary power supply; and

FIG. 2 illustrates an exemplary power supply circuit for such an integrated circuit chip.

Detailed Description

An exemplary LED print head has a row of 27 LED dice placed end-to-end to stretch across the width of a photoreceptive surface. Each die has 96 LEDs along its length. The LED dice are made in large numbers on a gallium arsenide wafer, which is then cut up to form the individual dice. It is found that there are variations in LED light output as a function of current from wafer to wafer, due to differences in processing variables. Generally speaking, all of the LEDs on a die are quite similar to each other in this characteristic. Dice from different wafers may differ appreciably in light output as a function of current. Dice from various portions of a wafer may fall somewhere in between. The light output from the LEDs on a large number of LED dice tends to have a more or less Gaussian distribution around a desired light output.

To minimize this source of non-uniformity in LEDs used in practice of this invention, the light output for each LED die is measured and the dice are sorted into collections or "bins" so that all of the dice in a given bin have a variation in light output much smaller than the variation occurring in the total population of LED dice. When a given print head is assembled, all of the dice are taken from a single bin so that the intrinsic light output as a function of current is substantially the same for all of the LEDs on that print head. What is next needed is a power supply that delivers the same current to each LED in the array.

Power is supplied to the LEDs from integrated circuit chips mounted in close proximity to the LED dice. In an exemplary embodiment, an integrated circuit chip is located on each side of each LED die and contains circuits for delivering current to half of the LEDs on the LED die. Thus, an exemplary integrated circuit chip may have 48 current sources for the respective LEDs. Such a chip may include a variety of other print head operational circuits which do not form a part of this invention. For example, data signal multiplexing circuits may be included on the chip.

Thus, as indicated schematically in FIG. 1, there may be a row of integrated circuit chips $IC_1, IC_2 \dots IC_n$ mounted near a row of LED dice (not shown). A system reference voltage, V_R is applied

to each of the integrated circuit chips. The magnitude of the system reference voltage can be set so that the light output from a given LED print head achieves a desired level. For example, if the intrinsic light output from the set of LEDs on the print head is lower than the mean of the Gaussian distribution of light outputs, the reference voltage for that print head might be a higher value than for a print head having an intrinsic light output closer to the mean. Thus, by varying the system reference voltage, the light output of all of the LEDs in the array can be raised or lowered, as desired, in synchronism.

In addition to variations that may occur due to processing variables of the LED dice, there may be variations in the properties of the integrated circuit chips supplying power to the LEDs. The LED print heads are analog devices and such variations may be more significant than in digital circuits.

To compensate for such possible variations, each integrated circuit chip is provided with a reference resistor, $R_1, R_2 \dots R_n$, the value of which may be selected for assuring that all of the integrated circuit and LED sets on a given print head produce the same light output. It is desirable that the reference resistor be located external to the integrated circuit chip for selection at a late stage in assembly of a print head. Typical resistance values for the reference resistors lie in the range from 100 to 600 ohms. If desired, the reference resistors may be variable rather than being selected from a range of resistance values. It turns out in practice that careful selection and attention to manufacturing techniques produces sufficiently uniform light output that only occasional changes in resistance values may be appropriate.

A portion of the circuits on a representative integrated circuit chip are illustrated in FIG. 2. In this drawing, a phantom line indicates the portion of the circuit lying on the chip as distinguished from components such as a reference resistor R , which, as just mentioned, is preferably located off of the integrated chip. Contact pads for making connections to the chip are omitted, as are many other details of circuits on such a chip which are not material to an understanding of this invention.

The reference voltage, V_R , is applied to the non-inverting input to a difference amplifier 10 (commonly referred to as an op-amp) of a reference current cell on each chip. There is nothing remarkable about the op-amp current controller, and its internal circuits are, therefore, not illustrated. It comprises a conventional comparator circuit, an output buffer for the comparator, a compensation capacitor to prevent oscillations, and a bias circuit for the comparator and buffer. The op-amp circuits are formed by the same processes employed for the balance of the circuitry on the

integrated circuit chips.

The op-amp output is connected to the gate of an n-channel insulated gate field effect transistor 11 (IGFET or FET, sometimes referred to as a JFET) which acts as a current regulator or current limiter. The source of the n-channel current regulator FET is connected to both the inverting input to the op-amp and the external reference resistor R_r . The drain of the current regulator FET is connected to the drain of a p-channel current source reference FET 12. The reference FET 12, as well as other components which provide current to the LEDs, are powered by a current supply voltage, V_c , common to the entire print head.

In the reference current cell the op-amp controls the gate of the n-channel current reference FET and increases or decreases the gate voltage until the voltage at the reference resistor matches the external reference voltage V_R at the non-inverting input to the op-amp. The result is a chip reference current equal to V_R/R_r . Thus, the reference current cell produces a chip reference voltage V_r at the drain of the reference FET 12. The internal or chip reference voltage V_r is not the same as the external or system reference voltage V_R . The reference current cell, with selection of a reference resistor, effectively eliminates chip-to-chip output current variations due to normal variations in integrated circuit chip processing parameters. An exemplary operating range for the chip reference voltage is from zero to 2.5 volts for a current supply voltage V_c of 5 volts.

The chip reference voltage V_r is tied to the gate of the reference FET. It is also connected to the gate of each of a plurality of similar p-channel output driver FETs 13₁, 13₂ . . . 13_n, which provide current for respective light emitting diodes 14₁, 14₂ . . . 14_n. By having the gates of all of the output driver FETs 13 tied together to the chip reference voltage V_r , the current for each driver is substantially identical. These can be thought of as current mirrors with the same current flow as in the constant reference current cell, or if desired, scaled to a uniform different current by having different parameters for the output FETs 13 as compared with the parameters of the reference FET 12.

Each of the output drivers 13 is in series with a p-channel data FET 15₁, 15₂ . . . 15_n. The data FETs act as switches in response to presence or absence of a data signal D_1 , D_2 . . . D_n applied to the gate of the respective data FET. By having independent drivers for each LED, the light output, rise time and the like is substantially identical for all of the LEDs. The current from each driver, and the respective rise and fall time, for each LED is substantially independent of the number of LEDs enabled. Nominal values for the reference resistor and chip reference voltage generate a nominal

output current of about 5.0 milliamps. Average current may vary as little as ± 3 percent. A variation as little as ± 1 per cent can be achieved with somewhat poorer yield of end product.

5 The output or LED current can be maintained within ± 0.5 per cent from nominal over a range of variation in the supply voltage V_c in the order of ± 0.25 volt from a nominal 5 volts. The design provided in practice of this invention permits the average light output across the whole print head to be adjusted by means of the system reference voltage V_R . A reference resistor, R_r , is used to adjust light output uniformity by adjusting individual integrated circuit/LED die pairs. Thus, there is one reference resistor for each of the integrated circuit chips employed in the print head.

10 Another important parameter in generating quality print is exposure time. In order to keep the exposure time the same regardless of the number of LEDs turned on, it is important to minimize the difference in rise and fall times with respect to the number of drivers enabled. The series data gate design, where each LED has a current mirror power source and a data switch, largely eliminates rise and fall time differences. These times may be the same within 20 per cent whether one or all of the drivers are turned on.

15 Although but one embodiment of power supply for an LED print head has been described and illustrated herein, many modifications and variations will be apparent to one skilled in the art. Thus, for example, instead of having a reference resistor off of the integrated circuit chip, such a resistor could be formed on the chip and trimmed as appropriate by conventional techniques.

20 The specific FETs and other parts of the power supply have been described in the preferred embodiment with certain characteristic parameters. It will be apparent that these are merely exemplary. For example, p-channel FETs are used in the drivers and it will be apparent that the circuits can be modified to use n-channel FETs, MOSFETs, or bipolar transistors to accomplish the same purposes. It will, therefore, be understood that within the scope of the appended claims the invention may be practiced otherwise than as specifically described.

25 50 Claims

30 35 40 45 50 55 60 65 70 75 80 85 90 95 100 105 110 115 120 125 130 135 140 145 150 155 160 165 170 175 180 185 190 195 200 205 210 215 220 225 230 235 240 245 250 255 260 265 270 275 280 285 290 295 300 305 310 315 320 325 330 335 340 345 350 355 360 365 370 375 380 385 390 395 400 405 410 415 420 425 430 435 440 445 450 455 460 465 470 475 480 485 490 495 500 505 510 515 520 525 530 535 540 545 550 555 560 565 570 575 580 585 590 595 600 605 610 615 620 625 630 635 640 645 650 655 660 665 670 675 680 685 690 695 700 705 710 715 720 725 730 735 740 745 750 755 760 765 770 775 780 785 790 795 800 805 810 815 820 825 830 835 840 845 850 855 860 865 870 875 880 885 890 895 900 905 910 915 920 925 930 935 940 945 950 955 960 965 970 975 980 985 990 995 1000 1005 1010 1015 1020 1025 1030 1035 1040 1045 1050 1055 1060 1065 1070 1075 1080 1085 1090 1095 1100 1105 1110 1115 1120 1125 1130 1135 1140 1145 1150 1155 1160 1165 1170 1175 1180 1185 1190 1195 1200 1205 1210 1215 1220 1225 1230 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means for applying a system reference voltage (V_R) to each of the integrated circuit chips (IC);
 means for generating a chip reference voltage (V_r) for each of the plurality of chips (IC) in response to the system reference voltage (V_R); and
 means for applying a current to each LED output which is a function of the respective chip reference voltage (V_r) and the presence or absence of a data signal (D) for such LED output.

2. A power supply according to claim 1 further comprising means (10,11,12,R_r) for setting the chip reference voltage (V_r) for a respective chip (IC) to a value for equalizing current output for all of the integrated circuit chips (IC).

3. A power supply according to claim 2 wherein the means for generating a chip reference voltage comprises a constant current source (10,11,12) and a reference resistor (R_r) for setting a constant current from such source.

4. A power supply according to any of claims 1 to 3 wherein the means for applying current for each LED (14) comprises a data FET (15) and a output driver FET (13) in series, the gate of the output driver FET (13) being connected to the chip reference voltage (V_r) and further comprising means for applying a data signal (D) to the gate of the data FET (15).

5. A power supply according to claim 4 wherein the constant current source comprises a FET (12) substantially the same as the output driver FETs (13) and having the chip reference voltage (V_r) applied to its gate.

6. A power supply for a light emitting diode print head comprising:
 a reference FET (12);
 a plurality of output driver FETs (13) substantially similar to the reference FET (12), each having an output for connection to an LED (17);
 means for interconnecting the gates of all the FETs (13) with the output of the reference FET (12);
 means (10,11,R_r) for passing a constant current through the reference FET (12); and
 means (15) for enabling the current flow from each output driver FET (13) to its respective LED (14) in response to a data signal (D).

7. A power supply according to claim 6 wherein the means for passing a constant current comprises an op-amp regulated current source (10,11) and a reference resistor (R_r) for setting the current.

8. A power supply according to claim 6 or 7 wherein the means for enabling current flow comprises a data FET (15) in series with each output driver FET (13) and means for applying a data signal (D) to the gate of the data FET (15).

9. A power supply for a light emitting diode print head comprising:
 a plurality of integrated circuit chips (IC), each chip

having a plurality of outputs for supplying current to each of a plurality of respective LEDs (14);
 means for applying a system reference voltage (V_R) to each of the integrated circuit chips (IC);

5 means for applying a current source voltage (V_c) to each of the integrated circuit chips (IC); and
 a reference resistor (R_r) connected to each integrated circuit chip (IC);

10 each such integrated circuit chip (IC) comprising:
 a reference current source (10,11,12,R_r) connected to the system reference voltage (V_R) for providing a chip reference voltage (V_r);
 a plurality of data transistors (15), each being connected to a respective LED output;

15 means for applying a data signal (D) to the gate of each data transistor (15);
 a plurality of output driver transistors (13) connected to the current source voltage (V_c) and to a respective data transistor (15); and
 20 means for connecting the chip reference voltage (V_r) to the gate of each output driver transistor (13).

10. A power supply according to claim 9 comprising a reference resistor (R_r) connected to the reference current source (10,11,12) on each integrated circuit chip (IC) for setting the current in the reference current source (10,11,12) on the respective chip.

11. A power supply according to claim 10 wherein each transistor (13,15) is a field effect transistor.

12. A power supply according to any of claims 9 to 11 wherein the reference current source (10,11,12) provides the reference voltage (V_r) at the drain of a p-channel FET (12) having its drain connected to its gate.

13. A power supply according to claim 12 wherein the output driver FETs (13) are also p-channel FETs and each of the output driver FETs (13) has its drain connected to a respective data FET source (15).

14. A method for obtaining uniform light output from a plurality of light emitting diodes (14) on an LED print head comprising the steps of:
 sorting a plurality of LED dice, each having a

45 plurality of LEDs (14) thereon, according to their light output as a function of current;
 placing a plurality of such LEDs (14) having substantially the same light output as a function of current in a row on a substrate;

50 placing a row of integrated circuit chips (IC) on the substrate beside the row of LED dice;
 providing current to each LED (14) from a separate output driver (13) on one of the integrated circuit chips (IC);

55 controlling the current from all the output drivers (13) on each integrated circuit chip (IC) with a chip reference voltage (V_r); and
 adjusting the chip reference voltages (V_r) for in-

dividual chips (IC), if required, for providing substantially the same light output from each LED die.

15. A method according to claim 14 comprising providing a system reference voltage (V_R) to all of the integrated circuit chips (IC) for controlling all the chip reference voltages (V_r) in synchronism.

16. A method according to claim 14 or 15 wherein the step of adjusting a chip reference voltage (V_r) comprises selecting a resistance value in a constant current source (10,11,12,R_r) for providing a desired chip reference voltage (V_r).

17. A method according to any of claims 14 to 16 comprising switching current from each output driver (13) to its respective LED in response to a data signal (D).

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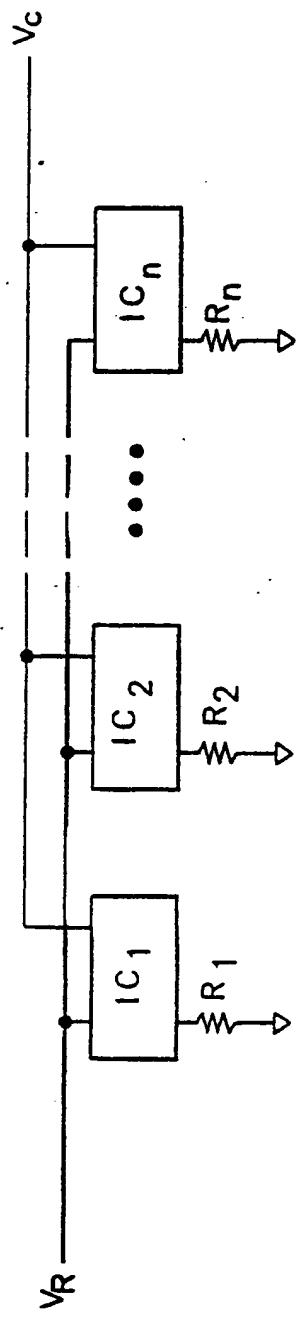
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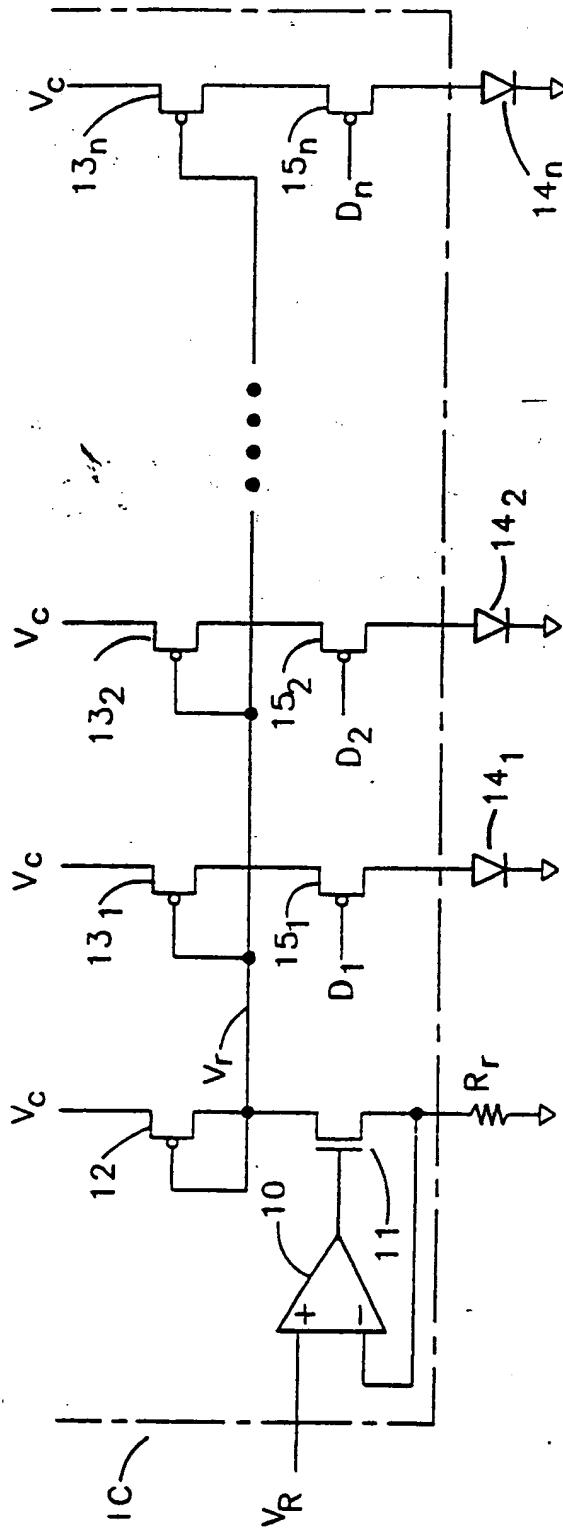
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FIG



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FIG



EP 90 30 0304

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	WO-A-8807729 (EASTMAN KODAK) * the whole document *	1-3, 6, 14-17	GN6K15/12
Y	---	4-5, 8-10	
Y	DE-A-3813664 (HITACHI) * page 4, line 41 - page 6, line 66; figures 1-3, 7 *	4-5, 8-10	
X	IEEE TRANSACTIONS ON CONSUMER ELECTRONICS. vol. CE-32, no. 1, February 1986, NEW YORK US pages 26 - 31; J.BURKHART et al.: "A monolithically integrated 128 LED-driver and its application" * the whole document *	1-3, 9-10	
A	---	6, 14-17	
TECHNICAL FIELDS SEARCHED (Int. Cl.5)			
G06K			

The present search report has been drawn up for all claims

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Place of search

THE HAGUE

Date of completion of the search

26 APRIL 1990

Examiner

GYSEN L.A.D.

CATEGORY OF CITED DOCUMENTS

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